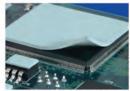
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Electronics Cooling

March 2012



Forward Looking

Jim Wilson, Editor-in-chief

4 COOLING MATTERS

Google expansion, toothache cure, energy-efficient cooking, diary dates and more

6 THERMAL FACTS & FAIRY TALES

Heat Spreading Revisited
Clemens J.M. Lasance, Associate Technical Editor

8 TECHNICAL BRIEF

Ionic Winds: A New Frontier for Air Cooling Rakshit Tirumala and David B. Go

12 CALCULATION CORNER

Transient Modeling of a High-Power IC Package, Part 2 Bruce Guenin, Associate Technical Editor

Electronics COOLING 2012 BUYERS' GUIDE

PRODUCTS & SERVICES INDEX.35 COMPANY DIRECTORY......38

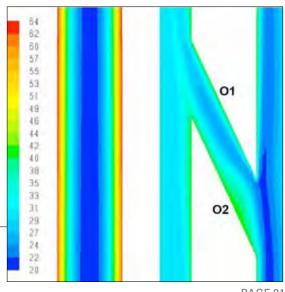
Feature Articles

20 THERMAL GROUND PLANE VAPOR CHAMBER HEAT SPREADERS FOR HIGH POWER AND PACKAGING DENSITY ELECTRONIC SYSTEMS

David Altman, Justin Weibel, Suresh Garimella, Timothy Fisher, Jason Nadler, Mark North

28 ENHANCEMENT OF MICROCHANNEL COOLING WITH OBLIQUE TECHNOLOGY

P.S. Lee, X.X. Kong, Y.J. Lee, L.W. Jin



40 INDEX OF ADVERTISERS

PAGE 31

Editorial

Forward Looking

Jim Wilson, Editor-in-Chief, March 2012



N THE LAST WINTER ISSUE OF ELECTRONICS COOLING,

our friend and colleague Bob Simons used his editorial column to inform us that he was resigning his position as an associate technical editor and this issue will be his last. The title of Bob's editorial was "Looking Back" so it seems fitting to include the term forward in the title of this editorial and to use this space to both thank Bob and introduce our newest associate technical editor.

Most of us like to have a measurement of how we are progressing or at least some relevant data that we hope relates to value. Most managers will try and have some metrics that they use as indicators of success and trends. Most good managers realize that if they start overly emphasizing the measurement, they may get a desired value, but not necessarily the desired result. It can be difficult to assign a precise value to the different measures of success. In case you are wondering how this relates to Bob and our newest technical editor, my belief is that there is value in both the measured accomplishments (number of papers, etc.) as well as the subjective opinion.

Bob Simons has influenced the field of electronics thermal management for the past 45 years and for the past 11 years, specifically *Electronics Cooling*. When I started to write this editorial, I remembered that a brand-new co-worker attended a SEMI-THERM short course taught by Bob in 2001. I went by his desk and he still had the notes to the class that are still relevant today. Bob's past mentoring contributed to currently valuable thermal engineers. It has been my pleasure to work with Bob and have respect for his engineering and thorough editing expertise. Bob, we are grateful and wish you the best.

Now to look forward. We are pleased to welcome another great mind and thermal expert as an associate technical editor: Madhusudan Iyengar of IBM. Madhu (as he is known to his friends and associates) has been a contributing author to *Electronics Cooling* in the past and I am sure many of you are aware of his accomplishments and reputation. The words on this page are mostly at the discretion of the editor in charge of the particular issue, but I do have objectives, namely that you keep reading and for this column to be one page. From a limited-length standpoint, a full description of Madhu's accomplishments in the field of electronics cooling is impossible. However, here are some of his highlights. Madhu is currently a Senior Engineer with the Advanced Thermal Lab, System & Technology Group of IBM, where he works on advanced thermal design and energy-efficient concepts for servers and data centers. Prior to joining IBM, Madhu conducted post doctoral research at the Cooling Technologies Research Center at Purdue University, emphasizing design for manufacturability related to high performance heat sinks. Madhu's Ph.D. is from the University of Minnesota and his research focused on efficient air cooling.

In the subjective category, Madhu comes with the full endorsement of his fellow associate technical editors and we are proud to have him as part of the team. We welcome Madhu's perspective and look forward to him putting his personal touch on *Electronics Cooling*.

So, a fond farewell to Bob and a warm welcome to Madhu.

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Cooling Matters

Applications of thermal management technologies

Energy Harvesting Cooking Sensor Cuts Energy Use in Half

Most domestic appliances carry an energy label indicating their efficiency in using electrical energy, and new technologies for energy-saving cooking are expected to be part of a new EU directive because they are considered a major energy savings potential.

Multisensory cooking technology by MSX Technology and

Micropelt GmbH could help in this effort since it has proven an energy reduction for

cooking of 50% or more, confirmed by the Swiss Federal Laboratories (EMPA) and VDE, the German Association for Electrical, Electronic & Information Technologies.

A wireless sensor is embedded in the lid of a cooking vessel, transmitting temperature and acoustic data and controlling the cooking process for best result at minimal energy consumption. The user just pushes a button to start the fully automated cooking process.

Source: IDTechEx

Google's Plans to Expand **Headquarters Spark Intrigue**

Further inflaming rumors that Google is getting into the hardware business are the company's plans to build at least two new labs and an "Experience Center" at its Mountain View, Calif., headquarters.

Mike Swift at the San Jose Mercury combed through public construction records and found plans for:

> · A lab to test electronic devices under the Google/@home brand, including screening out radio frequency

signals to test new wireless technology consumer thermal and anechoic chambers that could be used to test antenna patterns;

A Project X lab that involves highprecision optics and would include the use of rare gases, a plasma cleaner, and arcane optical-coating technology;

120,000-square-foot "Experience Center" to share its inventions with important partners and potential customers.

Source: San Jose Mercury

NTE Materials Research Could Offer Toothache Cure

One common reason that people with fillings experience toothache is that their fillings expand at a different rate to the original tooth when, for example, drinking a hot drink. Contrary to intuition, however, not all materials expand when heated; some actually contract. Recent research on these so-called negative thermal expansion (NTE) materials has led to the discovery of alloys exhibiting unexpectedly large thermal contraction.

Controlling the thermal expansion of composites is important for producing nanometer-scale electronic circuits. An ability to combine NTE materials with "normal" materials that expand upon heating ensures a reduction in thermal expansion in a composite material something that people with tooth

> example is Invar (left), an iron-nickel alloy with a low coefficient of thermal expansion.

fillings would appreciate. An

Source: National Institute for Materials Science

Diary Dates

April 16 - 18

May 8 - 10

May 14 - 17

March 18 - 22

SEMI-THERM 28 DoubleTree Hotel, San Jose, Calif., USA semi-therm.org

EuroSimE 2012

Hotel Cascais Miragem, Lisbon, Portugal www.eurosime.org

International Conference on **High Temperature Electronics** (HiTEC 2012)

Albuquerque Marriott Pyramid North, Albuquerque, N.M., USA www.imaps.org/hitec

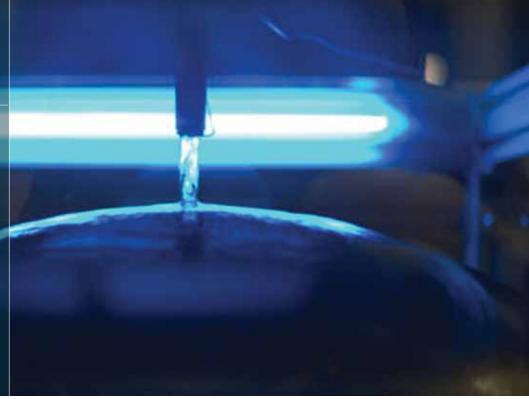
Uptime Institute Symposium 2012 Digital Infrastructure Convergence

Santa Clara Convention Center, Santa Clara, Calif., USA symposium.uptimeinstitute.com

'Thermal Battery' Chills Milk in India

Promethean Power has developed a novel energy storage system that uses a cold liquid to rapidly chill milk even when there's no power from the grid available. The company's first three cold storage systems will be deployed in India this year at processing centers. By having these types of systems, milk collection points can take milk from small farmers for longer hours, thereby giving them more opportunity to earn money. Seen here is a test system using water at Promethean Power's South Boston workspace at Greentown Labs, a clean-tech incubator. The warm milk, brought directly after milking, is poured over the cylinder-shaped tank and quickly brings milk to 4 degrees Celsius.

Photo by Martin LaMonica/CNET



CLOUD COMPUTING PROMPTS WORLDWIDE DATA CENTER EXPANSION

3-5 Billion

The amount of kronor (\$440-\$734 million) Facebook is expected to pump into its first European data center, and third globally, in the Swedish town of Luleaa. The location was chosen because of its "suitable climate for environmental cooling (and) clean power."

Source: AFP

"IT'S ALL VERY CLOAK-AND-DAGGER STUFF."

Clyde Evans, economic development leader of West Des Moines, Iowa

West Des Moines, already home to a \$200 million Microsoft center, has fielded interest recently from three or four site selectors looking to locate large data center projects. It could be Apple, Facebook, or even the U.S. government that's considering lowa for a \$1.2 billion data center, say industry experts.

Source: Des Moines Register

WHO ELSE IS PLANNING TO BUILD OR EXPAND DATA CENTERS IN THE NEXT FEW YEARS?

Online Tech reports: An energy-efficient, cloud computing data center on 11 acres in Dublin, Ireland, with a \$101 million investment; a \$200 million investment in three data centers in Asia — Singapore, Hong Kong & Taiwan; a new \$600 million, 130,000-square-foot data center in Pryor, Okla. This is in addition to projects by Microsoft in Dublin, West Des Moines and Boydton, Va.; Facebook in North Carolina and IBM in Langfang, China.

Мау	[,] 29 - Jun. 1	May 30 - June 1	Sept. 18 - 19	Nov. 26 - 30	
Technology Sheraton S	Components and y Conference (ECTC) an Diego Hotel and n Diego, Calif., USA .net	ITherm 2012 Sheraton San Diego Hotel and Marina, San Diego, Calif., USA www.ithermconference.org	Advancements in Thermal Management 2012 Denver, Colo., USA www.thermalnews.com/ conf_12/TN12_index.php	2012 MRS Fall Meeting Hynes Convention Center, Boston, Mass. www.mrs.org/meetings	

Heat Spreading Revisited

Clemens J.M. Lasance, Associate Technical Editor

S PROMISED IN MY editorial of the 2011 Fall issue I will devote this issue's column on the topic of heat spreading. The reason is that I, while writing a white paper on basic thermal management for LED applications, found some unexpected facts pointing at problems of interpretation of heat spreading data for dual layers when using the often-used heat spreading equations.

In this magazine heat spreading has been a frequently discussed subject, the references [1-5] testify to this assertion. Despite this, I think it worthwhile to add some more words. Facts and fairy tales on heat spreading are all over the place and many contradictory conclusions can be found in literature. This does not mean that some conclusions are wrong, it means that some conclusions have only limited validity and cannot be extended to other boundary conditions, or dimensions, or physical properties. The real problem is that these limits are rarely explicitly mentioned and in some cases the data are presented in the form of correlations, introducing the danger that the original data are gone forever and only the dimensionless form survives. As an example, take my own article [4] titled "Heat Spreading, Not a Trivial Problem." Therein, an example was given extending the Song, Lee and Au (SLA) equations to a two-layer problem of an LED on a submount fixed to a heat spreader/ sink. While constraints were given

(such as h/k>10 m⁻¹) it was also stated that this rarely occurs in practice. This conclusion was right for the category discussed, but not for other categories such as a common one I encountered doing some work for a company that sells metal-core PCBs. I was asked to write a white paper for the APEX/IPC conference in Las Vegas, on the subject of basic thermal management of LEDs on MC-PCBs.

Most of you probably know what an MC-PCB basically consists of: a thin dielectric (say 0.1 mm) on top of a metal core (say 1.6 mm). The manufacturer was wondering which requirement from a thermal point of view his MC-PCB should have, and he was especially wondering if the leading manufacturers were right in stating that a higher thermal conductivity of the board would improve its thermal performance. Well, I doubted this statement from the very beginning, maybe fuelled by my bias towards the claims of many vendors. One of the excesses of capitalism is that plain lies are taken for granted. Applying a simple 1D series resistance network, it becomes immediately obvious that of all elements in the chain (LED, MC-PCB, TIM, heat sink, convection), the MC-PCB is the last one to attack, unless you are dealing with top-of-thebill (and hence high-power) LEDs, and liquid cooling. Such a 1D approach is commonly used to address first-order LED thermal management but the question is if heat spreading effects are going to change the conclusions based on such an approach.

We are dealing here with a two-layer problem, hence, I initially thought I could simply fall back on the approach sketched in [4] to address the heat spreading issues. However, I soon realized this was a dead end. First of all, the conditions were not met to warrant their use. Second, the basic idea behind using the extended SLA equations [6] is that the second layer acts as an effective heat transfer coefficient boundary condition for the first layer, the implicit assumption being that the first layer acts as a heat source for the second layer. For the case submount/spreader as discussed in [4] we may indeed assume that the submount area acts as a heat source. The SLA equations calculate a total R_{th} based on two parts: a spreading resistance and a convective resistance, where the convective area is the whole area. However, this is not the right approach for the case at hand. Consider a very thin layer with low thermal conductivity, a small source on top of it and convection at the bottom. Obviously, there is almost no heat spreading, and the area that dissipates heat into the air is not the layer area but rather the source area. In order to apply the SLA double-layer equation this latter area is needed but this one is not calculated. By the way, the same is true for the very handy tool available at the website of the University of Waterloo [7]. When dealing with only one layer, as was the intention of the SLA equations, it should be realized that these calculate a thermal spreading resistance using the total area. While

this is apparently wrong in the case of the thin dielectric layer, it does not matter for the calculation because the calculated total R_{th} is OK. However, it does matter for a two-layer case because then you need the area explicitly.

Hence, the problem boils down to the following. While the SLA equations and the UoW tool can be used for twolayer heat spreading, we cannot get a feeling for the magnitude of the individual contributions. From a designer's point of view we need this split, because she wants to get a feeling for all elements in the chain in order to make the right design decisions. The approach chosen is the following: for the dielectric thermal resistance, a spreading resistance is used based on some spreading angle rule, from which also the source area follows that is subsequently used to calculate the heat spreading in the metal using the SLA equations. In practice, to get a first-order estimate, it is sufficient to take for the dielectric the LED area and neglect the spreading in the metal layer. Optimal spreading angle and other accuracy issues will be covered in a paper to be published soon [8].

Finally, please keep in mind that, looking at the wide range of boundary conditions, physical properties, and dimensions that occur in practice, no general rule of thumb can be given, simply because physics does not allow us to separate the convection and conduction parts.

Here is my final advice: for early design phases, use a spread sheet approach, to get a first (and usually pretty accurate) feeling. For the final design phase, use dedicated tools such as CFD codes in conduction-only mode.

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Ionic Winds

A New Frontier for Air-Cooling

Rakshit Tirumala & David B. Go, Department of Aerospace and Mechanical Engineering, University of Notre Dame, Notre Dame, Indiana, USA

IR-COOLING IS THE oldest and, in many ways, the easiest method of cooling electronics, whether it be through fan-driven forced convection or simply natural convection. However, with the increasing speed of processors, shrinking of form factors, and expansion of device functionality, air-cooling has begun to find itself on the outside looking in as new cooling technologies are sought, and new developments in solid-state cooling, such as thermoelectrics, and liquid cooling, such as two-phase microchannels, have become more popular in the heat transfer research community. Yet, because it is cheap, easy-toimplement, and a "known quantity," forced convection air-cooling remains a very attractive approach for thermal management. Further, since in most cases, particularly in portable electronic devices, the entire heat load is eventually dissipated to ambient air, it will always be a critical component of any cooling solution. However, because of the ongoing evolution of electronics and new technologies such as light emitting diodes, advances in air-cooling technologies must also keep pace.

Classically, forced convection is driven by the conversion of mechanical-to-fluid energy through a fan. However, these fans often come with power consumption and acoustic penalties, as well as issues associated with moving parts and challenges facing miniaturization. This is especially pertinent in

the areas of portable electronics and light emitting diodes (LEDs). One promising new approach is to utilize electrical-to-fluid energy conversion, a so-called ionic wind, where bulk air motion is not generated by a rotating or flapping mechanical structure but by a gas discharge. Recent advancements have shown that ionic winds could be a new frontier of air-cooling.

HISTORY OF IONIC WINDS

An ionic wind (also called an electric wind or corona wind) is generated when a gas discharge is formed between two electrodes in ambient air (Figure 1). Gaseous ions formed in the discharge are accelerated by the electric field and undergo collisions with neutral gas molecules. This exchange of momentum, and the subsequent cascading effect, generates a bulk fluid motion called an ionic wind. The ionic wind phenomenon has been known for centuries dating all the way to Francis Hauksbee who first observed the "electric wind" phenomena in 1709, with such notable scientists such as Sir Isaac Newton, Benjamin Franklin, and Michael Faraday all recording their own observations of the effect [1]. In the modern era, ionic winds began to be considered for practical engineering applications with two seminal publications by Stuetzer [2] (1959) and Robinson [3] (1961), who developed some of the fundamental relations still used today. Though investigations for heat transfer applications enjoyed some attention in the 1960s and 1970s, it is only recently that it has become a viable technological option, with companies such as Tessera Technologies, Inc. [4] and Ventiva [5] exploring technologies for laptop and LED cooling.

There are two types of gas discharges typically used to generate an ionic wind - the direct current (DC) corona discharge and the alternating current (AC) dielectric barrier discharge both of which are favored because of their inherent stability at atmospheric pressure. A corona discharge (Figure 1a) is formed when high potential is applied between a sharp (corona) and blunt (counter) electrode. The high degree of inhomogeneity in the electric field due to the asymmetric electrodes causes a partial breakdown of the air gap and a small plasma region to be formed around the sharp electrode.

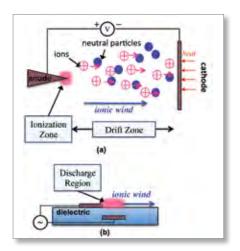


FIGURE 1: (a) Schematic of positive mode point-to-plane corona discharge-generated ionic wind. (b) Schematic of dielectric barrier discharge ionic wind, also called a plasma actuator.



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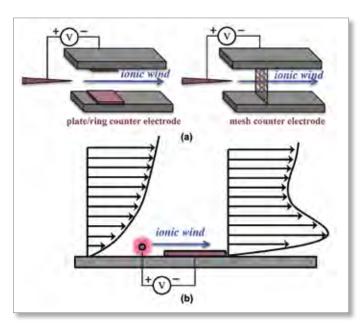
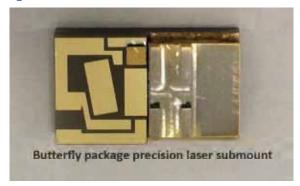


FIGURE 2: (a) Schematics of two common electrode configurations for ionic wind duct blowers. (b) Schematic of using an ionic wind aligned with a flow to modulate the boundary layer. The ionic wind forms a wall jet that accelerates the flow to thin the boundary layer and increasing convection.



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The remaining air gap then acts as a drift zone where the large ions drift from the plasma zone toward the counter electrode, and it is in this region where the ionic wind is generated. Corona discharges, and the subsequent ionic winds, can be generated in both positive mode (the corona electrode is the anode) and negative mode (the corona electrode is the cathode), as well as in pulsed modes. An AC dielectric barrier discharge (Figure 1b) is formed between two electrodes operated at oscillating high potentials, where at least one of the electrodes is covered by a dielectric, insulating material. This insulating material collects surface charges that serve to saturate the potential across the electrode gap and extinguish the discharge, thus preventing sparks. By using an oscillating field, the discharge is essentially extinguished and reignited every half-cycle. By using asymmetric electrode geometry, an ionic wind wall jet is formed along the surface of the insulating material.

There are a few drawbacks to both of these methods. Both require high potentials (> 1 kV) and generate ozone as a by-product of operation in atmospheric air. Further, because they are gas discharges, there is always the potential for sparking, which can be a safety issue; although with proper design and operation this can often be prevented with high certainty. In general, corona discharges have been favored for electronics cooling applications because the required DC electronics is often considered more practical for consumer products.

RECENT DEVELOPMENTS

For electronics cooling and heat transfer, an ionic wind can be produced and used in different ways. Original studies investigated directing the ionic wind at the hot surface, which also served as the counter electrode (Figure 1a), such that the impinging wind would directly cool the surface [6]. However, recent developments have focused on two operating modes – using the ionic wind as a ducted blower to replace a fan (Figure 2a) or using it to modulate a pre-existing flow (Figure 2b). In blower mode, a corona electrode is aligned with the axis of a duct and either a permeable mesh or surface counter electrode is used. The ionic wind generated between the two electrodes is then directed down the duct. A significant amount of effort has focused on duct and electrode design to maximize the raw flow rate through the duct, including creating multiple blowers in series, such that flow rates as high as 70 slm (standard liters/minute) have been reported [7]. One new development that shows significant promise is using multiple downstream collecting electrodes to form a so-called assisted corona discharge ionic wind [8]. This elongates the drift zone so that flow is more effectively generated, but avoids the necessity for very high applied potentials because the primary collecting electrode acts as a gate electrode.

In the presence of a pre-existing bulk flow, an ionic wind

can modulate the boundary layer, and, when aligned with the flow, it will accelerate the flow, thinning the boundary layer and enhancing convection. This area has received significant attention for drag reduction in aeronautics, often using dielectric barrier discharge plasma actuators [9]. Figure 3 shows infrared thermographic images of a heated glass plate when cooled by only natural convection, a low-speed pre-existing bulk flow (0.3 m/s), and a corona discharge ionic wind enhancing the bulk flow. For a constant heat flux of 4 W, when the bulk flow was superimposed with the ionic wind, an additional ~20 K of convective cooling was obtained, while the ionic wind consumed less than 100 mW of electrical power [10, 11]. Alternatively, the ionic wind can be directed perpendicular or opposed to the bulk flow such that the flow is distorted, to increase heat transfer [12]. In both of these scenarios, however, the use of an ionic wind is limited to laminar, low Reynolds number flows. Because the ion-neutral interaction acts as a Coulomb body force, if the inherent inertia in the pre-existing flow is too great, the ionic wind is ineffective. An analogous situation occurs in mixed convection, when the Grashof number is small compared to the square of the Reynolds number such that the buoyant body force becomes negligible.

THE FUTURE AND ONGOING **CHALLENGES**

Overall, ionic winds are a promising technology for a wide variety of important, but niche applications. Because of their poor electrical-tofluid energy conversion, an efficiency that ranges from 1-2% [13], they will be hard pressed to compete with mechanical fans for large scale cooling applications such as servers. But because they are inherently silent, consume little power, and offer the potential to be scaled to smaller dimensions, they may find application in portable devices, ranging from laptops to smart phones or consumer LED products. However, there continue to

be challenges. Future research must focus not only on improving flow generation and heat transfer, but also reducing the operating voltage, the degradation of electrodes over time, and minimizing ozone production, which are very real practical hurdles. Still, because of their potential advantages the future is bright that ionic winds will soon find a home in the cooling technologies market.

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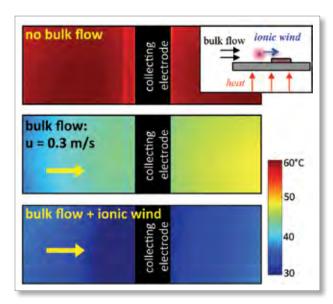


FIGURE 3: Infrared thermographic images of an ionic wind enhancing the cooling from a bulk flow [10, 11]. The ionic wind enhanced cooling by nearly 20 K while consuming less than 100 mW of power. A schematic of the experimental set-up is shown in the inset. For these experiments, 4 W of heat was applied to the plate to steady state, and then the plate was cooled by a 0.3 m/s flow to steady state by approximately 15 K. An 15 μA (67 mW) ionic wind was then superimposed on the bulk flow to reduce the plate temperature an additional 20 K. The average heat transfer was increased by a factor of 2 when the ionic wind was applied.

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Transient Modeling

of a High-Power IC Package, Part 2

Bruce Guenin, Associate Technical Editor

ART 1 OF THIS ARTICLE provided the groundwork for the present discussion [1]. It demonstrated the use of three different methodologies for performing a transient thermal analysis of a high-power IC package attached to a heat sink. These include the finite element analysis (FEA) method and a numerical model, which represents the package and heat sink using a multistage RC (resistor-capacitor) network. It presented the details for implementing the numerical model in a spreadsheet and demonstrated its accuracy using simplifying assumptions regarding the package and heat sink configuration - namely that all of these components have the same width as the die. Since the resultant one-dimensional heat flow can be represented quite accurately using simple analytical expressions, it provided a rigorous test of the ability of the numerical model to accurately represent the transient aspects of the heat transfer problem in this layered structure.

Part 2 applies the same two methodologies to a more realistic repre-

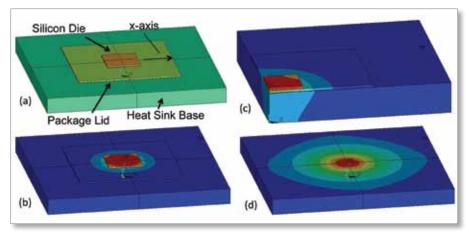


FIGURE 1: Images from FEA model: a) solid model, b-d) temperature contours, $HTC = 2000 \text{ W/m}^2\text{K}$; b) package and heat sink, full model; c) package and heat sink, quarter model; d) heat sink only, full model.

sentation of a high-power package attached to an air-cooled heat sink, in which the widths of the components are unequal (heat sink > package lid > silicon die) and the heat flow from the die to the ambient air via the heat sink is non uniform.

Furthermore, Part 2 uses a method, described in the context of a steady-state analysis of the same package, employing the concept of an isothermal Heat Transfer Area (HTA). The application of this concept enables a

simplified approach for calculating the thermal resistance of the package lid and thermal interface material between the lid and heat sink base (TIM2) and the heat sink-to-air thermal resistance [2, 3].

METHODOLOGY

Assumptions

The construction of the package and associated heat sink has been described in great detail in previous installments of this column [1, 2, 3]. Figure 1a illustrates the appearance of these components as represented in the FEA model.

In this model, two simplifying assumptions were made: 1) heat flow to the package substrate (attached to the reverse side of the die in the actual application) and then to the PCB is neglected (since in a high-power package

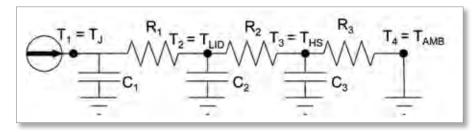


FIGURE 2: RC network topology for numerical model.

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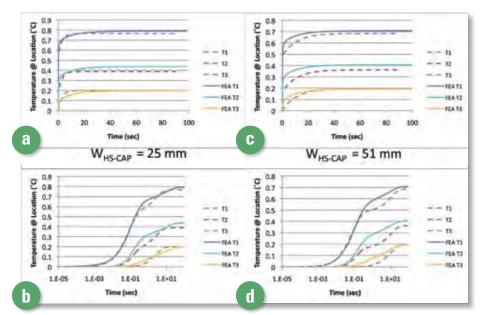


FIGURE 3: Comparison of FEA and numerical calculations. Effect of $W_{\text{HS-CAP}}$ variations. Linear and log time scales.

application it is of secondary importance) and 2) the cooling effect of heat sink fins is represented by the application of a suitable heat transfer coefficient (HTC) directly to the heat sink base. Hence, the only components explicitly represented in the model are the die, TIM1, lid, TIM2, and the heat sink base (where TIM = Thermal Interface Material). A wide range of HTC values is assumed in the analysis, ranging from 50 to 2000 W/m 2 K, as described in Table 2.

The quantitative assumptions of the model are listed in Tables 1 and 2. Note that the stated values of component dimensions, material composition, and thermal resistance are consistent with those in recent articles [1, 2, 3]. Note also that, for convenience, the ambient temperature was assumed to equal zero. Hence, the reported component temperatures correspond to the temperature rise with respect to the ambient. The power is assumed to increase in a stepwise fashion from 0W to 1W at time 0.

Numerical Model

The numerical model employed here uses a 3-stage RC network topology, as illustrated in Figure 2. Resistance values for each of the pure conduction components (die, TIM1, lid, and TIM2) are calculated assuming 1-D heat flow, using the following formula:

$$R_{CONDUCTIVE} = \frac{thickness}{width^2 * thermal conductivity}$$
(1)

where the value for the width is equal to a) the actual width for components experiencing a 1-D heat flow pattern (die and TIM1) or to b) the HTA Width for those components involved in a diverging flow situation (lid and TIM2). Note that a value of HTA Width of 17.5 mm has been calculated

for the current package in contact with the same copper heat sink, independent of the value of HTC [3]. R_1 and C_1 are each equal to the sum of the respective R and C values for the die and TIM1. The same relationship exists between R_2 and C_2 and the individual values of R and C for the lid and TIM2.

The heat sink-to-air thermal resistance (equal to R_3) is calculated using an analytical procedure described in Ref. [5], inputting the heat sink dimensions, thermal conductivity, the value of HTC, and the heat source size (equal to the HTA Width) [5].

The heat capacity of the pure conductive components is calculated from: C = Specific Heat * Density * Volume (2) where the volume is calculated using either the actual component width or the HTA Width as indicated above.

The calculation of the heat capacity of the heat sink is a bit more compli-

cated, since the network model assumes an isothermal temperature for the heat sink, corresponding to that of the $\rm T_3$ node in the diagram in Figure 2. However, the temperature distribution within the heat sink is often quite non-uniform, as we shall see. The only way these two facts can be reconciled is to assume an effective volume for the heat sink (smaller than the actual volume), which stores the same quantity of heat at a uniform temperature (essentially the maximum temperature in the actual heat sink) as is stored by the actual physical heat sink having a range of internal temperatures.

In this case, we define a new parameter $W_{\text{HS-CAP}}$ to represent the width of this effective energy storage volume such that the heat capacity of the heat sink, C_{HS} , is equal to

 C_{HS} = Specific Heat *Density * t_{HS} * W^2_{HS-CAP} (3) where t_{HS} is the heat sink thickness. C_3 in the network is set equal to C_{HS} .

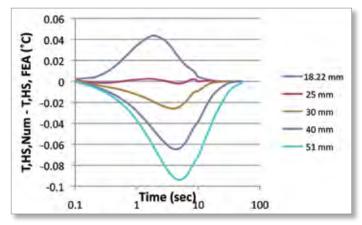
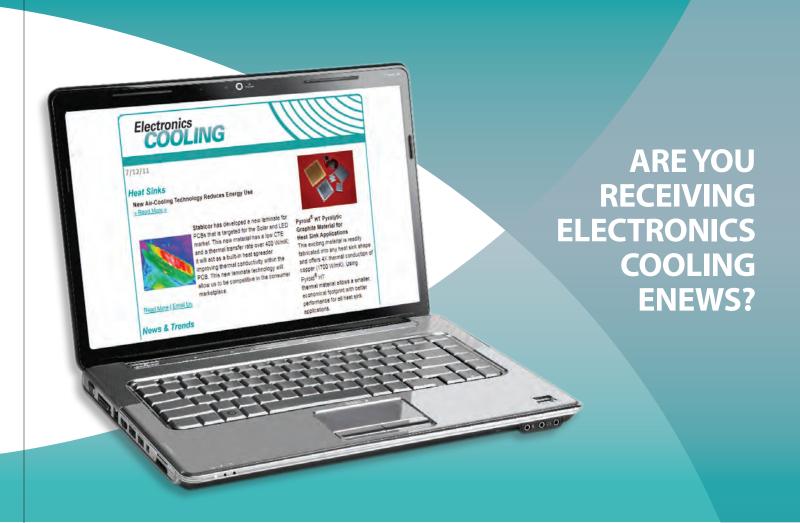


FIGURE 4: Plot of heat sink temperatures vs. time. Difference between numerical and FEA models at various values of $W_{\rm HS-CAP}$



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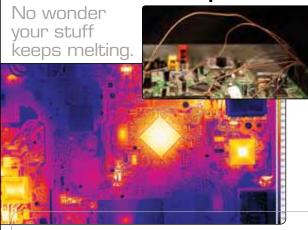
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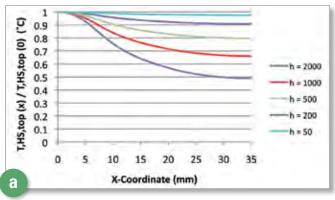
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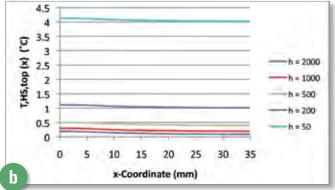
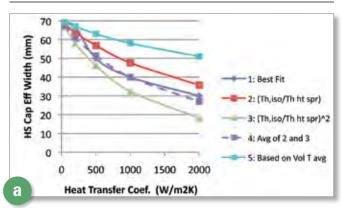


FIGURE 5: Plot of FEA results: heat sink temperatures at top of heat sink, along x-axis: a) values normalized to peak temperature; b) non-modified temperature values.



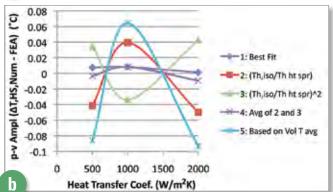


FIGURE 6: Comparison of 5 methods for generating $W_{\text{HS-CAP}}$ values. a) $W_{\text{HS-CAP}}$ values plotted versus the heat transfer coef. b) values of peak-to-valley amplitude of T,HS difference curves: numerical minus FEA results. Best performance at value = 0.

Calculating the appropriate value of C_uS is not straightforward due to the non-uniform heat sink temperature. In the following section, the sensitivity of the calculation accuracy to varying C_{HS} is determined. Various methods are explored for calculating the optimum value of C_{HS} (or equivalently, W_{HS-CAP}).

FEA Model

The results of the numerical model are compared with those from a commercial FEA software package [4]. For the purposes of this comparison, the FEA values are considered to represent an "exact" solution.

THERMAL SOLUTIONS

Figures 1b and 1c show the temperature contours, for a full and quarter model respectively, plotted on the surfaces of the package, lid, and heat sink, resulting from an FEA calculation at an HTC value of 2000 W/m²K. The complicated thermal gradient field in the heat sink

base should be noted, with components in both the vertical and radial directions, as is evident from Figures 1c and 1d.

Figure 3 contains graphs comparing solutions of the numerical model, for two different values of $W_{\text{HS-CAP}}$, 25 and 51 mm, with the FEA solution versus time. The results are plotted using both linear and logarithmic time scales. The results for each value of $W_{\mbox{\scriptsize HS-CAP}}$ manifest two different behaviors for the calculated value of heat sink temperature (T₂). In the log-time plot for the 25 mm case, the curve representing T₂ calculated by the numerical model shows an oscillation that is symmetrical about the FEA value of T_3 . On the other hand, that for the 51 mm case is smaller than the FEA value. These two behaviors result in a reasonably accurate transient behavior for the die (T1) for the 25 mm case, but one which is slower in response for the 51 mm solution.

Figure 4 plots the difference between the heat sink temperature curves calculated using the numerical model (at various values of $\boldsymbol{W}_{\text{HS-CAP}}\!)$ and one using the FEA method. The best agreement was achieved using the 25 mm value for $W_{\mbox{\scriptsize HS-CAP}}$. The peak-to-valley amplitude for each of these difference curves is used in a subsequent analysis to compare the relative accuracy of the numerical model calculated using different values of W_{HS-CAP} for situations involving other values of HTC.

Figure 5 compares temperature profiles (from FEA models) for the top surface of the heat sink along the x-axis calculated for different values of HTC. In Figure 5a the local temperature is divided by the peak temperature (occurring at x=0) having the effect of normalizing the peak

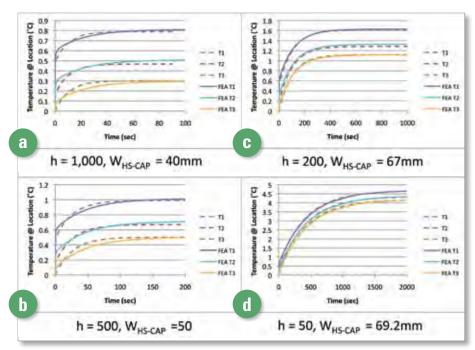


FIGURE 7: Comparison of FEA and numerical calculations at different values of HTC. Calculated at best-fit values of W_{HS,CAP}.



Layer No.	Compo- nent	Material	Actual Width	HTA Width*	Thick- ness	Heat Transfer Coef.	Thermal Resis- tance	Heat Capacity
			(mm)	(mm)	(mm)	(W/m2K)	('C/W)	(3/°C)
1	Die	Si	13	N/A	0,5	N/A	0.027	0.132
2	TIME	Ag-Epoxy	13	Linke.	0,1		0,296	0.030
3	Lid	Cu	90	17.5	0.5		0.0042	0.524
4	TIM2	Grease, Al	40	17,5	0.05		0,1633	0.034
5	Heat Sink Base	Ċ	70	17,5	6	50	4.180	Depends on value of W.HS,Cap
						200	1.120	
						500	0.502	
						1000	0.300	
						2000	0.200	

Note: HTA = Heat Transfer Area. Used to calculate thermal resistance and neat capacity heat injection area. [See Ref. 3]

TABLE 1: Component dimensions, composition, and calculated thermal resistance and heat capacity.

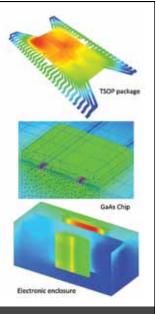
temperature to a value of 1. Quite clearly, the temperature is more highly peaked in the case of higher values of HTC and is much more uniform for the lowest values. This suggests that smaller values for $\boldsymbol{W}_{\text{HS-CAP}}$ would be appropriate for use in the numerical model at large values of HTC and conversely for small values of HTC.

Figure 5b plots the actual surface temperatures versus distance along the x-axis. As expected, the lower values of HTC lead to much higher average heat sink temperatures.

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Figure 6a shows calculated values of $W_{\mbox{\scriptsize HS-CAP'}}$ obtained by a variety of calculations. They are described below.

First a couple of definitions are provided to assist that discussion.

Th,iso = heat sink-to-air thermal resistance assuming an isothermal heat sink =

$$Th, iso = \frac{1}{W_{HS-Actual}^2 * HTC}$$
(4)

Th,spr = heat sink-to-air thermal resistance assuming a heat spreading situation =

 $Th.spr = f(W_{HS-Actual}, t_{HS}, k_{HS}, HTC, HTA \ Width)$ (5) where f is a function specified in Ref. [5] and k_{HS} is the thermal conductivity of the heat sink.

Competing Methods for Calculating W_{HS-CAP} :

- Best fit: for each value of HTC, determine by inspection, the value of $W_{\mbox{\tiny HS-CAP}}$ showing the minimum peak-to-valley amplitude for a curve of T_{HS-Num} – T_{HS-FEA}, as was demonstrated using Figure 3.
 - Use the ratio: Th,iso/Th,spr * W_{HS-Actual} 2.
 - Use the ratio: (Th,iso/Th,spr)² * W_{HS-Actual} 3.
 - Use the average of methods 2 and 3 4.
- Use the average HS temperature, calculated from FEA over the entire volume of the heat sink, and the following expression:

$$W_{HS-CAP} = \sqrt{\frac{T_{HS,Avg}}{T_{HS,Peak}}} * W_{HS-Actual}$$
(6)

Looking in detail at Figure 6a, one sees that, as expected, all the calculated values of $\boldsymbol{W}_{\text{HS-CAP}}$ decrease with increasing HTC. However, for values of HTC of 500 W/m2K and greater, there is a large disparity between the values of $\boldsymbol{W}_{\text{HS-CAP}}$ calculated using the various methods.

Material	Th. Cond.	Density	Specific Heat	
Moterial	(W/m-K)	(gm/cm ³)	(J/gm-C)	
SI	111	2.33	0.668	
Ag-Epoxy	2.0	4.40	0.400	
Cu	390	8.89	0.385	
Grease, with Al filler particles	1.0	2,50	0.900	
Environm	ental Condit	ions and The	rmal Loads	
Parameter	Value	Units	Surface Applied To	
Heat Transfer Coef	50, 200, 500, 1000, 2000	W/m²K	Bottom of Heat Sink	
Ambient Temp.	0	c	N/A	
Power	1.0	W	Top of die	

TABLE 2: Material properties.

Figure 6b shows the results from calculating the peakto-valley amplitude for curves of $T_{\mbox{\tiny HS-Num}} - T_{\mbox{\tiny HS-FEA}}$ for the higher HTC cases (500, 1000, and 2000 W/m²K). The best fit method shows a very small error as does method 4. The other methods show much worse behavior. This indicates a narrow range of values of $W_{\text{HS-CAP}}$ that can lead to the $T_{\text{HS,Num}}$ curve essentially lying on top of the curve for $T_{\text{HS-FEA}}$ (albeit with some oscillation present.) Furthermore, it is telling that using the value of $W_{\mbox{\scriptsize HS-CAP}}$ obtained using Method 5 (which was calculated using a conservation of thermal energy argument) leads to poor results for cases at these higher values of HTC. However, Method 5 works well at small values of HTC (50 and 200 W/m²K, where the heat sink temperature distribution is relatively uniform.

At the higher values of HTC, the temperature of the heat sink at the center is appreciably different from that at its edges. This suggests that the use of a single temperature node to represent the entire heat sink volume is too severe for these higher HTC cases. In these situations, it is to be expected that dividing the heat sink into at least two concentric regions represented by two nodes would provide a more robust model and reduce the incidence of oscillations of the $T_{\mbox{\tiny HS-Num}}$ curve about that representing T_{HS-FEA} . Hence, the exercise of controlling the value of $W_{\mbox{\tiny HS-CAP}}$ within a narrow range is one of "tuning" the phase of the numerical model with respect to the FEA model so that the numerical model essentially oscillates about it in the region of most rapid temperature rise of the heat sink.

Figure 7 contains graphs, comparing the FEA results with numerical results obtained using the best fit values of W_{HS-CAP} . Note that for HTC = 50 and 200 W/m²K, the best fit values correspond to those obtained using method 5, and that these curves demonstrate no oscillations, suggesting that the current RC network topology does an adequate job of representing the physics of the situation. At HTC equal to 500 and 1000 W/m²K, there is some oscillation of the numerical results about the FEA curves. However, the numerical curves oscillate in a symmetrical fashion, improving the phase coherence between the two curves.

CONCLUSIONS

A simple lumped element numerical model was shown to have reasonable accuracy when compared to FEA results subject to the choice of an appropriate value for W_{HS-CAP}.

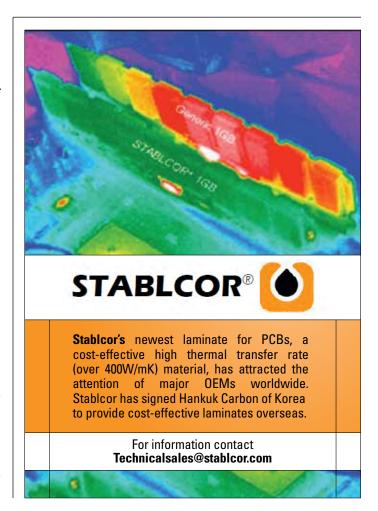
A technique was demonstrated for calculating near-optimum values of $\boldsymbol{W}_{\text{HS-CAP}}$, using the ratio of the isothermal and heat spreading values for the thermal resistance-to-air of the heat sink in a simple algebraic expression. However, it is not clear whether this is a robust correlation or merely fortuitous in this limited study. Further analysis involving other heat sink configurations (such as those in References 2 and 3) would be required.

The numerical model performs in a robust manner when the heat sink temperature distribution is relatively uniform. However, at values of HTC ≥ 500 W/m²K, obtaining reasonable phase coherence between the numerical and FEA models depends on choosing a value of at or near the best-fit value. Under these circumstances, the benefit of improving model stability would be well worth the effort to increase the number of nodes representing the heat sink.

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Thermal Ground Plane

Vapor Chamber Heat Spreaders for High Power and Packaging Density Electronic Systems

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Timothy S. Fisher received PhD and BS degrees in Mechanical Engineering from Cornell University in 1998 and 1991, respectively. He joined Purdue's School of Mechanical Engineering and Birck Nanotechnology Center in 2002 after several years at Vanderbilt University. Prior to his graduate studies, he was employed from 1991 to 1993 as a design engineer in Motorola's Automotive and Industrial Electronics Group.



Jason H. Nadler is a Senior Research Engineer at Georgia Tech Research Institute (GTRI). As head of the Multifunctional Materials and Structures Branch of GTRI's Electro-Optical Systems Lab, his principal research is in the design, synthesis and characterization of inorganic multifunctional materials. Nadler's work on the enhancement of transport properties through hierarchical, biologically informed architectures has led to research in a diverse array of applications.



Mark North joined Thermacore in 1993 after completing his PhD in Mechanical Engineering at Cornell University and is currently an Engineering Group Leader for R&D. He has over 20 years of experience in the development and application of heat pipe technology. He is currently an Associate Editor of the Journal of Thermal Science Engineering and Applications and has been a member of the ASME K-16 committee for over 10 years. North has published over 40 technical papers and holds 6 US patents.



INCE 2008, collaborative research conducted at Raytheon Company, Thermacore Incorporated, Purdue University and Georgia Tech Research Institute has pursued the development of Thermal Ground Plane (TGP) technology. The DARPAsponsored Radio Frequency Thermal Ground Plane (RFTGP) program focused on the development of high effective conductivity (k_{eff} >1000W/mK), low coefficient of thermal expansion (CTE, 5-7ppm/K), thin (1-3mm) heat spreaders for high heat flux (100+W/ cm²) cooling. RFTGPs were developed to spread heat from multiple small (10s of mm²) device sources over larger (10s of cm²) areas in high packaging density applications. Efforts focused on the development of CTE-matched vapor chamber designs incorporating high heat flux bearing, low thermal resistance evaporator wick structures. We produced a TGP that provides significantly improved performance relative to state-of-the-art commercial solid conductor solutions used in electronics packaging applications. This article highlights key results from the effort, including scientific findings, new capabilities, practical advantages and limitations associated with the developed TGP technology.

VAPOR CHAMBER HEAT SPREADERS

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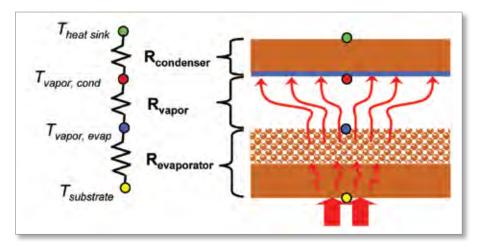


FIGURE 1: Component thermal resistances in vapor chamber heat spreaders.

transport devices that utilize capillary forces to circulate a working fluid between discrete evaporator and condenser regions. Waste heat from electronic devices affixed to the evaporator is absorbed through vaporization of a working fluid (in this case water) and subsequently rejected to a heat sink or cold plate attached to the condenser. Vapor chambers achieve high effective conductivity by transporting heat via vapor generation and flow, as opposed to conduction. Overall thermal resistance in vapor chamber heat spreaders is comprised of three primary component resistances: 1) evaporator resistance, 2) vapor transport (pressure loss) resistance, and 3) condenser resistance (Figure 1).

Although there are many ways vapor chambers (and heat pipes) can be used, most uses can be classified either as an "active cooling" mode, where the primary focus is X-Y heat spreading, or "remote cooling" mode, where the focus is lateral heat transport [1]. In the "active cooling" mode, the merit of a vapor chamber heat spreader relative to a solid conductor is primarily dependent on the evaporator resistance, geometry (thickness, condenser/ evaporator area ratio) and heat sink resistance (HSR) [2]. The HSR is defined by the packaging that separates the cold side of the TGP from the ambient, and can be approximated as the onedimensional combined conduction and

convection resistance from the cold side of the heat spreader to cooling fluid (liquid or air). Careful consideration of these factors for a given application is required to determine whether a solid conductor or vapor chamber solution will provide more effective heat spreading [3]. Ideal "active cooling" vapor chamber applications are those that are thickness-constrained with relatively large condenser/evaporator area ratios. Other considerations, such as a CTE-matching requirement, can also influence the solid conductor/ vapor chamber trade-off by limiting solid conductor options to lower conductivity ceramics (e.g., AlN) or metal composites (e.g., CuMo). While a thermal designer can sometimes define or influence geometry or HSR, often minimization of evaporator resistance is the primary opportunity to maximize the benefit achievable with a vapor chamber heat spreader. As such, much of our TGP development effort focused on development of high performance evaporator structures.

STATE OF THE ART SINTERED CU POWDER AND NANOSTRUCTURED EVAPORATOR WICKS

TGP resistance is heavily influenced by whether evaporative or boiling regimes dominate the vaporization process within the wick. During TGP development, evaporator investigations initially focused on the identification of novel ultra-thin and/or bi-porous wick designs to reduce evaporative resistance. However, for the small heat input areas (5mm x 5mm) and high heat fluxes (100s of W/cm2) investigated, we found boiling to be the higher performing and more desirable regime for TGP operation. This feature is attributable to the "short circuiting" of wick thermal resistance that occurs in the boiling regime (Figure 2). Experimental characterization of sintered Cu powder wicks demonstrated that it was possible to optimize sintered Cu powder wicks for boiling performance [4]. This was followed by investigation into novel wick structures specifically targeted to minimize resistance by promoting nucleate boiling. Two novel wick material systems were developed to maximize TGP performance.

CNT Enhanced Sintered Cu Powder Wick Structures

Due to their controllable nanoscale

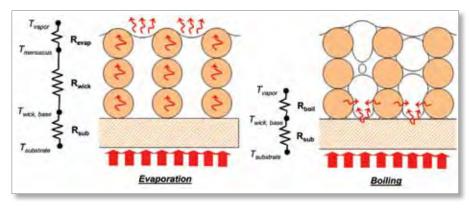


FIGURE 2: Evaporator thermal resistance in evaporation and boiling regimes.

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capillary features and high reported on-axis conductivity, vertically oriented Carbon Nanotubes (CNTs) were explored as a candidate nanowick material. Early in the TGP effort we demonstrated that CNTs (which are naturally hydrophobic) can be rendered hydrophilic through application of an evaporated, conformal Cu coating. However, we also found that the low permeability of CNTs overwhelms the increased capillary pumping, limiting their use to very small evaporator areas [5]. To overcome this limitation, several bi-porous wick design approaches involving selective CNT growth on patterned conventional sintered wick materials were analyzed, with results pointing to significant opportunities to reduce the evaporative resistance [6].

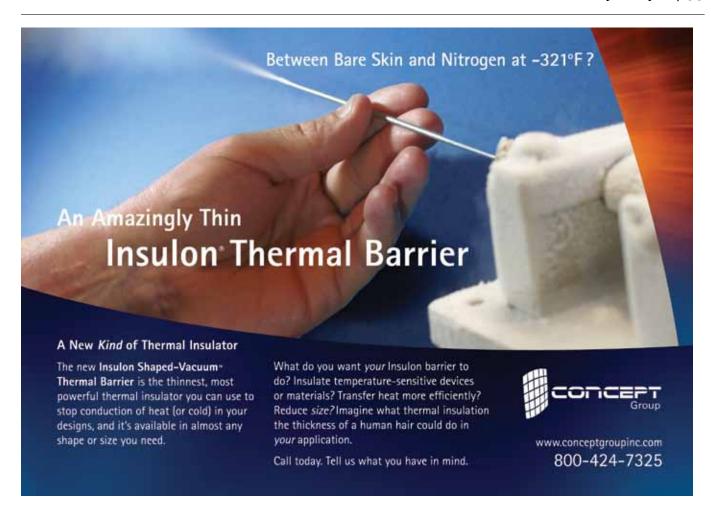
Experimental investigations were undertaken to determine whether these predicted benefits could be realized. While results indicated that

some potential enhancement could be achieved in the boiling regime [2], the predicted evaporative benefits proved difficult to quantify due to unforeseen (and beneficial) effects of CNT functionalization on heat transfer regime. In wicks functionalized with CNTs, we observed a significant decrease in the incipient wall superheat (peak temperature on the evaporator surface minus the vapor temperature) required to initiate boiling within the wick [7]. While CNTs had been demonstrated to reduce incipient superheat in pool boiling from flat surfaces [8], to our knowledge, this was the first conclusive demonstration of such a reduction in sintered Cu powder wicks. Moreover, we observed that the CNTs reduced the variability inherent to boiling incipience relative to that observed in sintered powder wicks without CNTs (Figure 3). As such, CNTs provide a means to ensure that the wicks would

operate in the boiling regime at lower heat inputs and at lower temperatures than they otherwise would without CNTs (Figure 5). This is a subtle but significant benefit. As shown in Figure 5, the transition from evaporation to boiling heat transfer within the TGP corresponds to a significant improvement in performance.

Cu Foam Wick Structures

State of the art (SoA) Cu powder-based wick structures are typically fabricated by sintering relatively large Cu particles (10s to 100s of microns) in a mold. Particle size and sintering conditions determine wick characteristics (i.e., effective pore radius, permeability, porosity), which largely determine TGP thermal resistance and transport capacity. We found that in thin (\sim 1mm) vapor chambers, the vapor phase pressure drop can become a significant limitation for transport capacity [9].



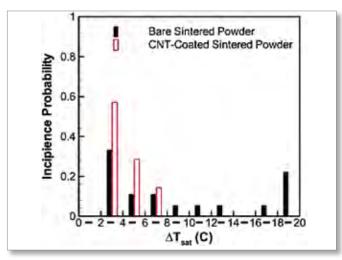


FIGURE 3: Probability of boiling incipience vs. wall superheat in sintered Cu powder wicks with and without CNT functionalization.

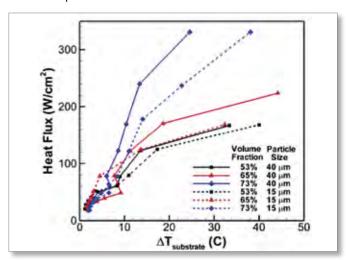


FIGURE 4: Thermal performance of 100µm thick Cu foam wicks with varying template size and volume fraction (Boiling Response Curve).

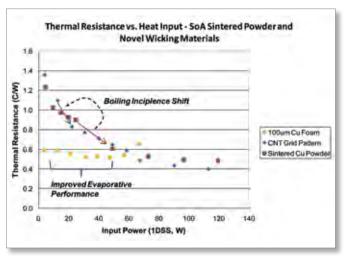


FIGURE 5: Measured performance characteristics of 3cm x 3cm x 3mm TGP vapor chambers with SoA sintered Cu powder and novel wick materials vs. one dimensional steady state (1DSS) heat input (5mm x 5mm centrally located heat source).

To avoid these limitations, novel fabrication approaches that provide thinner wicks with greater ranges of microstructural control were developed. These Cu foam-based materials were synthesized from precursor slurries containing micron-scale Cu particles and polymer template beads. Following slurry spin-coating of the substrate, thermochemical processing facilitates polymer burn-off and sintering of the remaining porous Cu structure. The resulting ~100µm thick wick structures exhibit multi-scale pore features defined by the volume fraction and size of the polymer template beads as well as the extent of inter-particle sintering. These wicks demonstrated both significantly reduced evaporative resistance (relative to conventional sintered Cu powder) in TGP testing (Figure 5) and significantly greater control of transport and thermal resistance characteristics in evaporator testing (Figure 4).

COMPARATIVE PERFORMANCE OF THERMAL GROUND PLANE AND SOLID CONDUCTOR HEAT SPREADERS

Ultimately, the value of a TGP is determined by its ability to provide higher performance at lower cost when compared to SoA and alternative emerging commercial solutions. To facilitate this comparison, our "Phase III" RFTGP design was compared head-to-head with solid conductors of equivalent size and CTE using the test facility described in [2]. The TGP provides substantial advantages relative to both SoA CuMo of identical CTE, with maximum benefit being obtained under boiling-dominated conditions (Figure 6). In the tested configuration, the TGP offers a peak effective isotropic thermal conductivity greater than 1200W/ mK, which is superior to that of all known composites and comparable to mid-grade polycrystalline diamond (Figure 7), which also exhibits a significantly lower CTE. It achieves this performance using low cost materials and fabrication techniques, while also retaining key attributes required for use in precision electronics packaging applications.

CONCLUSIONS AND FUTURE DIRECTIONS

Through the DARPA-supported Radio Frequency Thermal Ground Plane program, we have developed a cost-competitive CTE-matched vapor chamber technology that offers significant performance advantages relative to solid conductor alternatives. We have also demonstrated that CNTfunctionalized Cu powder and Cu foam wick structures can provide higher levels of performance and reduce sensitivity to operating conditions (e.g., heat input, temperature). While additional work is needed to mature these novel materials, the combined work conducted in our TGP effort has firmly established their utility. We expect the TGP technology developed in this effort to have a significant impact on both defense and commercial electronics packaging technology.

ACKNOWLEDGEMENT

The authors gratefully thank Dr. Thomas Kenny and Dr. Avram Bar-Cohen for their leadership and support on the Thermal Ground Plane program. This material is based upon work supported by the Defense Advanced Research Project Agency (DARPA) and Space and Naval Warfare Systems Center (SPAWARSYSCEN) San Diego, CA under Contract No. N66001-08-C-2011. The views expressed are those of the authors and do not reflect the official policy or position of the Department of Defense or the U.S. Government.

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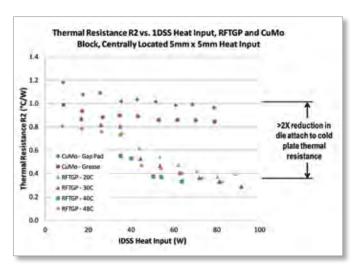


FIGURE 6: Comparative performance of Radio Frequency Thermal Ground Plane and a homogeneous CuMo heat spreader of equivalent dimensions vs. one dimensional steady state (1DSS) heat input for varying coolant temperatures. Note that Resistance R2 includes the heat spreader and interface to the instrumentation block directly below the heat spreader and that all RFTGP tests used a gap pad at that interface.



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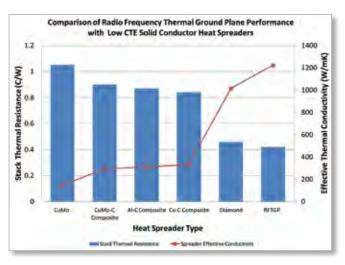


FIGURE 7: Comparative thermal resistance and equivalent isotropic thermal conductivity of RFTGP and competitive solid conductor alternatives. Geometry and heat source/sink boundary conditions identical for all materials.





Enhancement of

Microchannel Cooling with Oblique Technology

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P.S. Lee is an assistant professor of Mechanical Engineering at the National University of Singapore. He received his Ph.D. degree from Purdue University, West Lafayette in 2007. His current research interests include microfluidics and microscale heat transfer, high performance thermal management techniques (in particular microchannel singleand two-phase cooling) and hybrid solar energy harvesting



X.X. Kong is a mechanical engineer with GCoreLab Private Ltd, a thermal management solutions startup. He is currently working on the development and integration of an active high performance, energy efficient oblique fin microchannel cooling technology, and has conducted numerical modelings of fluid flow and heat transfer in liquid cold plates. He has also developed, fabricated and tested liquid cold plates to investigate thermal performance improvement.



L.W. Jin is a research fellow at the Department of Mechanical Engineering at the National University of Singapore. He received his Ph.D. degree in Nanyang Technological University of Singapore with a focus on fluid flow and heat transfer in metal foam. His current research interests are single- and two-phase heat transfer in microchannel and porous media, and thermal management of EVs/HEVs batteries.



ICROCHANNEL cooling has emerged as an effective method to enhance cooling for electronics devices [1]. However, the problem of boundary layer development, as liquid coolant travels downstream, persists in conventional microchannel heatsinks. Consequently, convective heat transfer performance of a heat sink deteriorates in the axial direction, resulting in elevated maximum temperature, and significant temperature gradient, across the heat sink. A heat transfer augmentation scheme is therefore beneficial for a microchannel heat sink.

A simple configuration of oblique cuts on the straight fins is developed to enhance the performance of the conventional heat sink [2]. The segmentation of continuous fin into oblique sections leads to the re-initialization of boundary layers at the leading edges of each oblique fin, effectively reducing boundary layer thickness. This regeneration of entrance effect causes the flow to maintain a thermally developing state throughout the channel, thus resulting in better heat transfer. In addition, the smaller oblique channels divert a small fraction of flow into the adjacent main channels [3]. The secondary flow thus created improves fluid mixing, which further enhances heat transfer. A pressure recovery effect is also noticed in the diffusive oblique channel, which minimizes the pressure drop penalty for the increased heat transfer.



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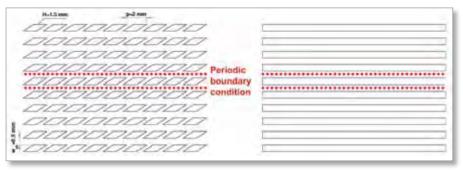


FIGURE 1: Schematic diagrams of (a) oblique fins and (b) straight microchannels.

BOUNDARY LAYER REDEVELOPMENT

The comparison of the enhanced oblique and conventional straight microchannels is shown in Figure 1. To understand the mechanism of enhancement of oblique technology, numerical simulation is carried to study the fluid flow and heat transfer characteristics. For a fair comparison, the straight microchannel share the same dimensions as the enhanced oblique microchannel with the exception of the oblique cuts on the continuous fins, i.e., similar channel aspect ratio, channel width, fin width and overall footprint.

Evident from Figure 1(a), the oblique fins structure exhibits spanwise periodicity if the edge effect is neglected. In the numerical simulation, the channel width (w_{ck}) , height (H) and fin pitch (p) are set to be 0.5 mm, 1.5 mm and 2 mm, respectively. The flow at a periodic boundary is treated as though the opposite periodic plane is a direct neighbor to the cells adjacent to the first periodic boundary. Thus, when calculating the flow through the periodic boundary adjacent to a fluid cell, the flow conditions at the fluid cell adjacent to the opposite periodic plane are used [4]. Both simulation domains for enhanced microchannel and conventional microchannel are generated using a general purpose CFD preprocessor [5]. The simulations are executed with a general purpose commercial CFD software [6], which solves the four governing equations numerically. These governing equations consist of continuity equation, momentum equation, energy equation for liquid and energy equation for solids.

Upon exporting the mesh files to the CFD software, the 3D doubleprecision pressure-based solver is

> selected with standard SIM-PLE algorithm as its pressurevelocity coupling method [7]. Standard discretization scheme is used for the pressure equation while second order upwind discretization scheme is selected for both momentum and energy equations. Water is

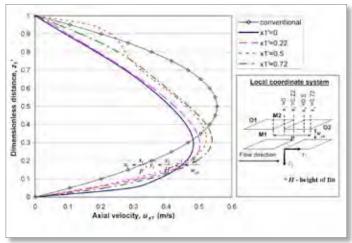
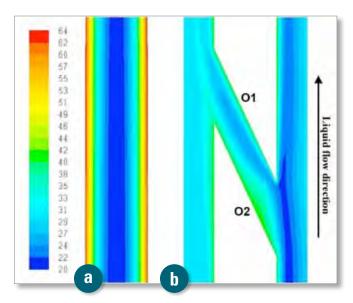


FIGURE 2: Axial velocity profiles at the mid-depth plane of microchannel heat sinks.



chosen as the working fluid while copper with constant thermal conductivity is selected as fins and heat sink material. The density, specific heat capacity, thermal conductivity and dynamic viscosity of water are evaluated at the mean fluid temperature [8].

To show the difference of flow conditions in straight and oblique microchannels, the axial velocity profiles are plotted in Figure 2 for the selected typical locations. Fin surfaces are also named specifically (M1, M2, O1 and O2). It is obvious that the segmentation of the continuous fin into sectional oblique fins disrupts the velocity profile and thus the hydrodynamic boundary layer development at the trailing edge of each oblique fin section. The discontinuity with the downstream fin causes the hydrodynamic boundary layer development to restart at the leading edge of the next downstream fin. In addition, the shorter oblique fin limits the development of boundary layer unlike the long continuous fin of the conventional microchannel. These axial velocity profiles of the enhanced microchannel skew towards fin surface M1 at $z_i' = 0$ in the main channel compared to the fully developed velocity profile of the conventional microchannel, which is symmetrical at the centerline $(z_i' = 0.5)$. Consequently, the oblique fins surfaces M1 at $z_i' = 0$ plane will have a thinner boundary layer even though the average velocity in the main channel is lower compared to that of the conventional microchannel. On the other hand, the oblique fins surfaces M2 on $z_i' = 1$ plane have a thicker hydrodynamic boundary layer.

Figure 3(a) displays the water temperature difference between two different structures. It is found that oblique channel presents more uniform fluid temperature distribution between 21 - 43°C compared to 20 - 62°C in the conventional microchannel as shown in Figures 3(a) and (b), due to better fluid mixing. The lower near-wall fluid temperature of the oblique channel translates to a lower channel wall temperature and a subsequently lower device temperature. As suggested by Steinke and Kandlikar [9],

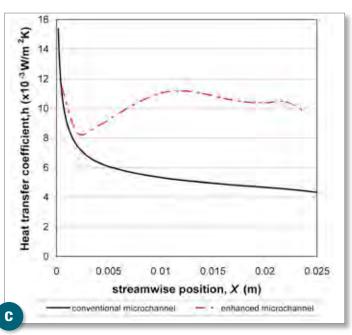
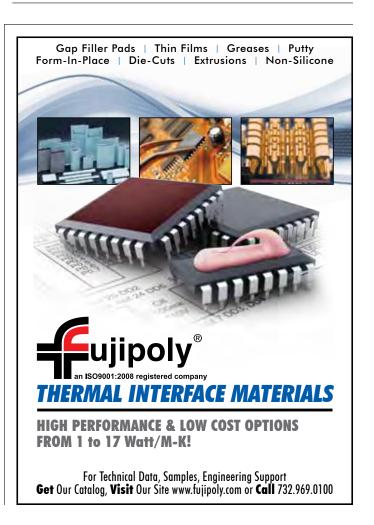


FIGURE 3: Temperature contour (in °C) of flow in (a) conventional microchannel;(b) oblique microchannel at X' = 0.5 and Y' = 0.5, and comparison in heat transfer coefficients (c).



the introduction of the oblique channel between the main channels could induce a small fraction of the liquid coolant into it and generate secondary flow, without requiring external power source or incurring a major increase in pressure drop. Subsequently, the injection of secondary flow back into the adjacent main channel disrupts the coolant flow, accelerates the heat propagation into the fluid core and increases the fluid mixing. It is also noted from Figure 3(c) that the initial heat transfer coefficient of the conventional microchannel can be as high as ~15,000 W/m²K. However, it quickly diminishes as the boundary layer thickens when the fluid travels downstream and attains a fairly constant value at ~5,000 W/m²K, displaying a highly non-uniform heat transfer performance from the inlet to the outlet of the heat sink. In contrast, the variation of local heat transfer coefficients for the enhanced microchannel is smaller, with a higher averaged value of ~10,000 W/m²K. The local heat transfer coefficient is observed to increase 100% almost anywhere along the channel. With the more effective heat transfer, 60% more heat flux can be dissipated with the enhanced microchannel if the same maximum wall temperatures are maintained.

TEST OF PROTOTYPE

The above numerical results show significant enhancement



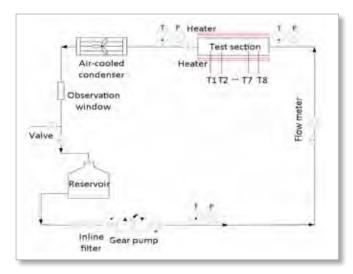


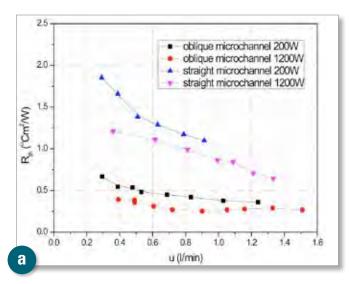
FIGURE 4: Schematic of experimental system.

due to the oblique structure. A liquid cold plate of a footprint 0.04752 m² and main channel hydraulic diameter of 2 mm with oblique microchannels is designed based on the practical applications for cooling battery packs used in an electronic vehicle. Due to the limitation of available space within the battery pack, an ultra-thin form factor is considered. In order to facilitate a fair comparison, a straight channel liquid cold plate is fabricated with the same dimensions only without oblique cuts on the fins. Figure 4 shows the test system flow diagram. The cooling liquid is driven by the gear pump, through the flow sensor and into the liquid cold plate, where heat transfer occurs. After that, the heated cooling liquid is cooled in the radiator before reaching the reservoir. The flow rate is measured with a flow sensor at the liquid cold plate inlet, while cooling liquid temperature and pressure are measured at both inlet and outlet of the liquid cold plate. Surface temperatures of the heaters installed on both sides of the cold plate are measured by 8 evenly distributed T-type thermocouples.

The total thermal resistance of tested cold plates are evaluated by

$$R_{th} = \frac{(T_{max} - T_{out})A}{Q} \tag{1}$$

where $T_{\rm max}$ and $T_{\rm out}$ are the surface maximum temperature and liquid outlet temperature. A and Q are the heating area and power. Figure 5(a) shows the calculated thermal resistance for the tested cases. The solid line represents the heating power of 200W while dash line corresponds to the heating power of 1200W. It can be seen that overall thermal resistance of oblique cold plates is much lower than that of straight channel for both power inputs. For the flow rate approximately ranging from 0.3 l/min to 0.9 l/min at 200 W, the averaged resistance of oblique channel is reduced 100% when compared to that of straight channel. For heating power of 1200 W, the thermal resistance of the oblique channel is about 3 times lower than that of



the straight channel. It indicates that the ultra-thin cold plate is a good candidate for high heat dissipation. A thermally enhanced microchannel heat sink should possess either better heat removal performance at a given driving power or lower pressure loss for a fixed heat duty. Figure 5(b) shows the pressure drop measured at cold plate inlet and outlet versus inlet flow rate. It is noted that the pressure drop of the oblique channel is similar to that of the straight channel. This implies that the pressure loss penalty for the increased solid-fluid contact area is negligible. As shown in Figure 1, the fluid flow in the secondary channels is induced naturally by pressure difference along the main channels. Compared to the main channel, the friction of secondary flow through the oblique channel is relatively small and does not require additional power. Therefore, the current proposed structure is a promising structure which can reduce the overall thermal resistance significantly while maintaining the pumping power unchanged when compared to the conventional straight microchannel.

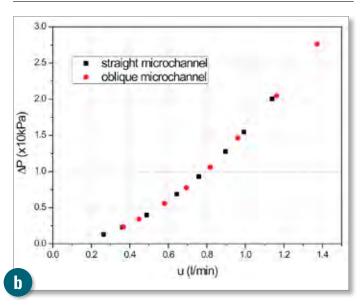
COST OF MANUFACTURING

Prototypes of two different footprints were discussed in the paper. The two footprints require different manufacturing methods. Metal injection molding (MIM) has been identified as a cost effective manufacturing methods for high volume production of the smaller footprint copper prototype. Each liquid cold plate is estimated to cost around 15 USD amortizing mold and assembly costs.

As for the larger footprint aluminum prototype, forging has been identified as a cost effective manufacturing method for high volume production. Each liquid cold plate inclusive of mold cost is estimated to range from 30 USD to 200 USD depending on assembly techniques.

As such, the enhanced oblique fin cold plate is a cost effective alternative to conventional liquid cold plates due to its comparable cost of production coupled with enhanced performance at lower pressure drop penalties.

FIGURE 5: (a) Overall thermal resistances and (b) pressure drop of oblique and straight microchannels.





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CONCLUSIONS

This article presents a novel structure to enhance convective heat transfer in the microchannel. Based on the analysis and experimental test on the proposed oblique technology, the advantages of using oblique microchannel for electronics cooling are as follows:

- 1) The oblique structure significantly increases the surface area for heat transfer between the cooling liquid and the solid material.
- 2) The fluid flow in the secondary channels interacts with the main channel flow causing it to be constantly in the development stage, thereby enhancing the performance of heat transfer.
- 3) The fluid flow in the secondary channels is induced naturally by pressure difference along the main channels. Due to extremely short flow path of the secondary flow in the oblique cut, the friction loss can be neglected and the system does not require additional power.
- 4) The oblique structure is simple and easy to apply to practical applications. In addition, cutting oblique slots on the continuous fins reduces the weight of heat sink/cold plate and the cost of material.
 - 5) Metal injection molding and forging can be used to

manufacture small and large footprint liquid cold plates respectively at competitive costs.

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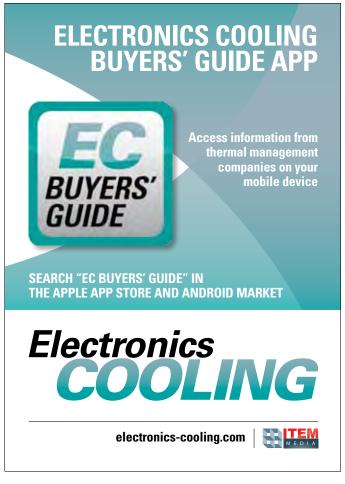
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0 0 1:4 : 1

Sanyo Denki America Inc Torrance, Calif., USA; 310-783-5456; www.sanyo-denki.com



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Sensor Products Inc.

Madison, N.J., USA; 973-428-8985; www.sensorprod.com

SEPA EUROPE GmbH

Freiburg, Germany; +49 761 3842273 0; www.sepa-europe.com

ShinEtsu MicroSi

Phoenix, Ariz., USA; 888-642-7674; www.microsi.com

Singapore Institute of Manufacturing Technology

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SinkPAD Corporation

Placentia, Calif., USA; 714-660-2944; www.sinkpad.com

Smart Heatsinks

Davie, Fla., USA; 954-840-3256; www.smartheatsinks.com

SNS Cooling Technology Inc.

Ontario, Canada; 416-738-7797; www.snscoolingtech.com

sp3 Diamond Technologies, Inc.

Santa Clara, Calif., USA; 408-492-0630;

www.sp3diamondtech.com



Spectra-Mat, Inc.

Member of SAES Getters Group, 100 Westgate Drive, Watsonville, CA 95076 USA; 831-722-4116; Fax: 831-722-4172; www.saes-smi-tmsolutions.com

Spectrum Sensors

St Marys, Pa., USA; 814-834-1541; www.specsensors.com



17011 Beach Blvd., Suite 900, Huntington Beach, CA 92647 USA; 714-375-6644; Fax: 714-375-6699; Don Roy, donroy@stablcor.com; www.Stablcor.com

Staubli Corporation

Duncan, S.C., USA; 864-433-1980; www.staubli.com

STEGO, Inc.

Marietta, Ga., USA; 770-984-0858; www.stegousa.com

Stockwell Elastomerics, Inc.

Philadelphia, Pa., USA; 215-335-3005; www.stockwell.com

Summit Thermal System Co., Ltd.34

Shin-lan Sec, Humen Town, Dong Guan, Guang Dong, P.R. China 523917: 86-769-8862-3990: Fax: 86-769-8862-3998: sales@summit.heat-sink.com.tw; www.heat-sink.com.tw

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Surmet Corporation

Burlington, Mass., USA; 781-345-5727; www.surmet.com

Taica Corporation

Tokyo, Japan; 81-3-6367-6624; www.taica.co.jp/english/ index.html

TE Technology, Inc.

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Temperature@lert

Boston, Mass., USA; 866-524-3540; www.temperaturealert.com

TES International LLC

Troy, Mich., USA; 248-362-2090; www.tesint.com

Tesea

Edison, N.J., USA; 732-225-9533; www.teseq.com



Thermacore, Inc.

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Thermal Engineering Associates, Inc.

3287 Kifer Road, Santa Clara, CA 95051-0826 USA; 650-961-5900; Fax: 650-227-3814; www.thermengr.com

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Thermal Solutions Inc.

Ann Arbor, Mich., USA; 734-761-1956; www.thermalsoftware.com

Thermshield LLC

Division of Niagara Thermal Products, PO Box 1641, Laconia, NH 03247 USA; 603-524-3714; info@thermshield.com; www.thermshield.com



Timtronics

35 Old Dock Road, Yaphank, NY 11980 USA; 631-345-6509; Prakash Khatri, info@timtronics.com; www.timtronics.com

TTM Co., Ltd.

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Universal Air Filter Company

Sauget, III., USA; 618-271-7300; www.uaf.com



Vette Corp.

Portsmouth, N.H., USA; 508-804-5509; www.vettecorp.com

Verotec Inc.

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Wakefield Solutions

Pelham, N.H., USA; 603-635-2800; www.wakefield.com

Waypoint Thermal Management, Inc.

Montville, N.J., USA; 908-672-7362; Fax: 973-257-8999; www.waypointmanagement.com

Wolverine Tube Inc. - MicroCool Division

Decatur, Ala., USA; 760-672-2567; Fax: 256-580-350; www.microcooling.com



Youthen Technology Co., Ltd.

GuangDong, China, 523921; 86-769-8161 9568; www.youthentec.cn

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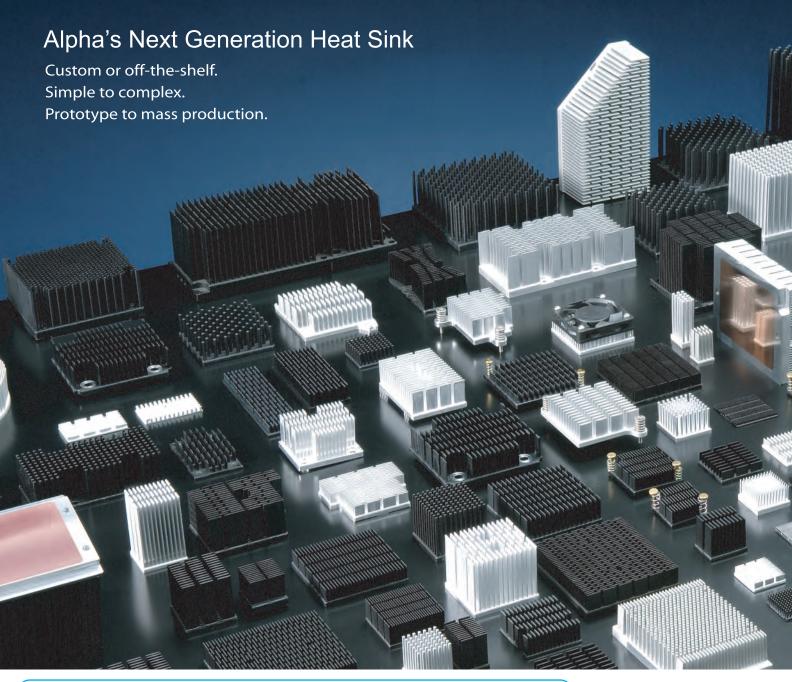
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Index of Advertisers

Future Facilities27
Harley Thermal LLC18
Intermark USA27
Interference Technology21
Malico Inc. 9
Mentor Graphics29
Mintres BV10
NMB Technologies13
PLANSEE SE
SEMI-THERM23
Stablcor Technology, Inc
Summit Thermal System Co., Ltd34
Sunon Inc3

Future Facilities	27
Harley Thermal LLC	18
Intermark USA	
Interference Technology	21
Malico Inc.	9
Mentor Graphics	29
Mintres BV	10
NMB Technologies	13
PLANSEE SE	7
SEMI-THERM	23
Stablcor Technology, Inc.	19
Summit Thermal System Co., Ltd	
Sunon Inc.	3





Minimum PCB Area

Pins only require 1.8mm diameter holes in the PCB.



Mounting Security







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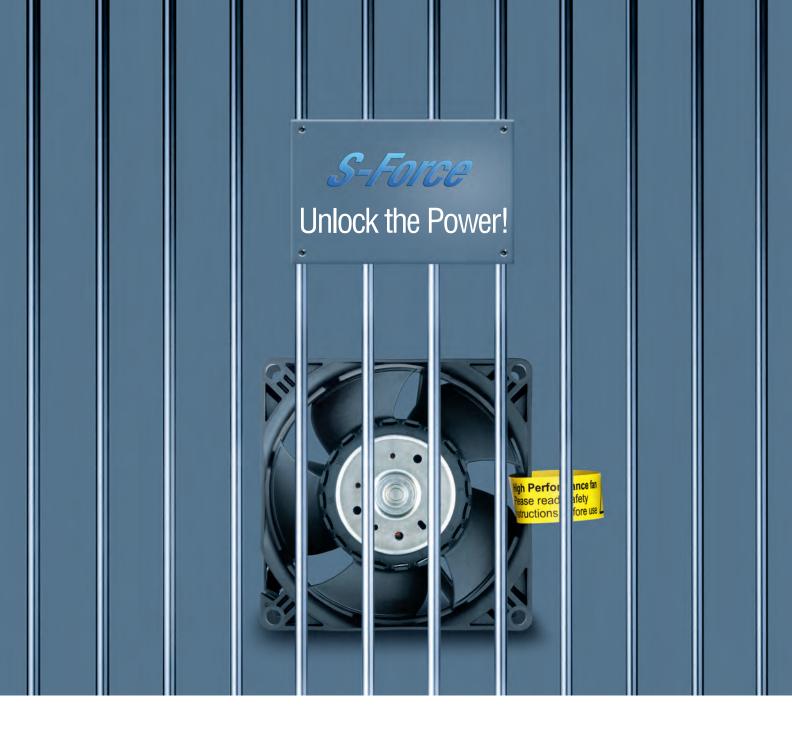


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